

9-06-05

IFW



Atty. Dkt. No. 045054-0158

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Hitoshi IRINO

Title: SEMICONDUCTOR PROTECTION  
ELEMENT, SEMICONDUCTOR  
DEVICE AND METHOD FOR  
MANUFACTURING SAME

Appl. No.: 10/796,999

Filing Date: 03/21/2004

Examiner: Lee, Calvin

Art Unit: 2818

**CERTIFICATE OF EXPRESS MAILING**

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September 2, 2005

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Christine Flores

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(Signature)

**RESPONSE TO RESTRICTION REQUIREMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the Office Action dated August 3, 2005 applicant hereby elects the claims of Group B, claims 1-16 and 26-41.

Applicant reserves the right to file a divisional application covering the subject matter of the non-elected claims.

Receipt of the initial Office Action on the merits is awaited.

Respectfully submitted,

Date September 2, 2005

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By

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